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11 Number of Pages (including this page)

Date: August 25, 2005

To: Tu Tu V. Ho - 2818

Location: United States Patent and Trademark Office

Fax No.: (571) 273-8300

From: Ranjeev Singh - 47,093

Subject: 10/790,420 – Jian Chen et al.

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MESSAGE:

Enclosed herewith, please find a FORMAL AMENDMENT for filing in the below-identified application.

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

1.	x	1 page Facsimile Cover Sheet
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EXAMINER: Tu Tu V. Ho
 GROUP ART UNIT: 2818
 SERIAL NO.: 10/790,420
 FILED: MARCH 1, 2004
 INVENTOR: JIAN CHEN ET AL.

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UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S) Jian Chen et al. GROUP ART UNIT: 2818
APPLN. NO.: 10/790,420 EXAMINER: Tu Tu V. Ho
FILED: March 1, 2004
TITLE: INTEGRATED CIRCUIT WITH MULTIPLE SPACER INSULATING
REGION WIDTHS

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AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated July 12, 2005, and Examiner's comments with regard thereto, please enter the following amendments in the above-entitled application, without prejudice or disclaimer.